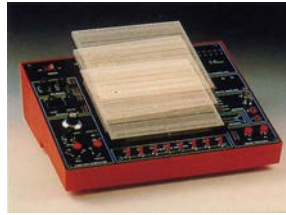




ETS-5000 Advanced Digital Training System



Feature
Specially Removable

AD-222 solderless breadboard can be easily put into and taken off. It is convenient for keeping the individual experiment.

Reserve Fixed Holders
It is ideally suitable for universal connectors.

Specifications

- Solderless breadboard : AD-222
Interconnected nickel plated 2712 tie points, fits all components with DIP sizes and solid wire AWG #22-30 (0.3~0.8mm). It can be changed and replaced for different purposes and can be connected with demonstration panel. Therefore, it is very convenient for both teachers and students.
- DC power supply :
 - Fixed DC output : +5V, 1 A
 - Fixed DC output : -5V, 300mA
 - Variable DC output : +3V ~ +15V, 500mA
 - Variable DC output : -3V ~ -15V, 500mA
- Mode selector switch :
When the switch is put on "TTL" or "CMOS" position, the input or output of pulse generator, pulser switches, 8 bits data switches, digital probe, 8 bit LED display will meet the HI or LO level of "TTL" or "CMOS"
- Two digits of 7-segment LED display
- Pulse generator :
 - Duty cycle : 50%
 - Frequency range :
 - 1 Hz ~ 10 Hz
 - 10 Hz ~ 100 Hz
 - 100 Hz ~ 1 KHz
 - 1 KHz ~ 10 KHz
 - 10 KHz ~ 100 KHz
 - 100 KHz ~ 1 MHz
 - Amplitude : 0 ~ 10Vpp
 - TTL/CMOS mode output
TTL : +5V
CMOS : +VDC (depends on the + VDC output)

- Eight bits LED display :
Set mode selector switch to "TTL" position

Logic Level	Input Level	Display light up
LO	<0.8±0.2V	Green
HI	>2.3±0.2V	Red
Open	0.8~2.3V	No display

Set mode selector switch to "CMOS" position

Logic Level	Input Level	Display light up
LO	<30%+VDC ±10%	Green
HI	>70%+VDC ±10%	Red
Open	30%~70%+VDC	No display

- Two pulser switches :
 - A, \bar{A} , B, \bar{B} output
 - Output level :
TTL : HI = 5V, LO = 0.1V
CMOS : HI = + VDC, LO = 0.1V

- Eight data switches :
TTL : HI = 5V, LO = 0V
CMOS : HI = +VDC, LO = 0V

- Digital probe :
Set mode selector switch to "TTL" position

Logic Level	Input Level	Display
LO	<0.8±0.2V	L
HI	>2.3±0.2V	H
Open	0.8~2.3V	O
Transit	LO-->HI	P

Set mode selector switch to "CMOS" position

Logic Level	Input Level	Display
LO	<30%+VDC ±10%	L
HI	>70%+VDC ±10%	H
Open	30%~70%+VDC	O
Transit	LO-->HI	P

Memory : The two points besides 7 segment LED display will keep lighting while in "level transition"(LO --> HI or HI--> LO)

- Universal connector :
It reserves fixed holders on the panel in order to be connected with various connectors, for example :

Standard accessory :
UC-02 RS-232 D sub connector (25pin female & male)

Optional accessories :

- UC-01 : Card edge connector 2.54mm 62pin
- UC-03 : Straight header connector 60pin
- UC-04 : Card edge connector 3.96mm 56pin

- Other standard accessories :

- Power cord
- Test probe
- Pin : 6pcs
- User manual

- Dimension :
(325(L) x 250(W) x 95(H))mm

- Weight : 4.3kg