



## LP-1001 Logic Probe



1. Testing TTL, DTL, HTL, CMOS and MOS
2. Displaying pulse presence and logic states
3. Catching pulse to 10ns or pulse trains to 50MHz
4. Input overvoltage protection

### Specifications

Max. allowable supply voltage	±25V
Guaranteed operation supply Voltage range	+3V ~ +18V
Input voltage	±50V
Operating input voltage range	-25V ~ +25V
Input impedance	120KΩ typical
Minimum detectable pulse width	10ns typical
Logic thresholds	
Logic 1 (green LED lights)	TTL 2.3V ±0.2V CMOS 70%Vcc ±10%
Logic 0 (green LED off)	TTL 0.8V ±0.1V CMOS 30%Vcc ±10%
High Impedance (green LED flash at frequency of 1 Hz)	200KΩ typical (5V)
Input positive voltage (red LED lights)	+5V Max
Input negative voltage (yellow LED lights)	-5V Max
Max input frequency displayable	50MHz Typical
Input overvoltage protection	±50V / 10sec.
Pulse LED (yellow flashing time)	1sec.
Power supply protection	±25V / 15sec.
Dimensions	16Ø x 160mm ±5%
Weight	Approx: 50g

## LP-2001 Logic Pulser



1. For TTL, DTL, RTL, HTL, CMOS and MOS
2. Producing 10µs pulse signal
3. Overload protection
4. Output current up to 100mA (pulse)

### Specifications

Max. allowable supply voltage	±25V
Operating voltage range	+3V ~ +18V
Max. Output allowable connection voltage	±25V
Max. sync. input voltage	±120V / 15sec.
Sync. input impedance	100KΩ Min
Pulse repetition rate	0.5Hz / 500Hz
Pulse width at pulser output pin	10µs Typical 100mA Load
Output current: pulse mode	100mA sink / source
50 %pulse mode	4mA sink / source
Power supply protection	±25V / 15sec.
Dimensions	16Ø x 160mm ±5%
Weight	Approx : 54g